

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,898,144 B2
APPLICATION NO. : 10/624772
DATED : May 24, 2005
INVENTOR(S) : Eric T. Stubbs and James E. Miller

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The Title page, showing an illustrative figure, should be deleted and substitute therefor the attached Title page.

In the drawings:

In FIG. 3,

insert reference numeral --260-- and associated lead line with arrow, located on the right side of the drawing.

Replace FIG. 3 with the following amended figure:

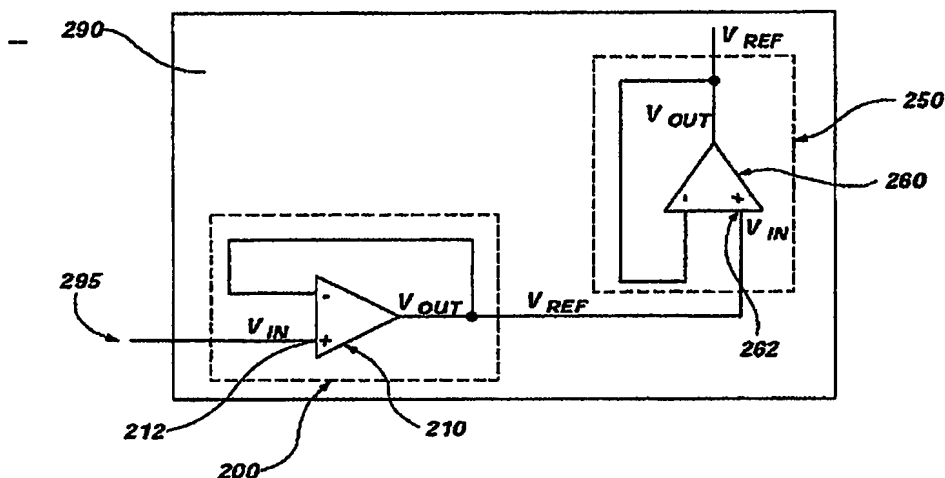


Fig. 3

Signed and Sealed this

Twentieth Day of November, 2007

JON W. DUDAS

Director of the United States Patent and Trademark Office

(12) **United States Patent**
Stubbs et al.

(10) Patent No.: **US 6,898,144 B2**
(45) Date of Patent: **May 24, 2005**

(54) **ACTIVELY DRIVEN VREF FOR INPUT BUFFER NOISE IMMUNITY**

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(73) Assignee: Micron Technology, Inc., Boise, ID (US)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: 10/624,772

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Related U.S. Application Data

(63) Continuation of application No. 09/759,499, filed on Jan. 12, 2001, now Pat. No. 6,597,619.

(51) Int. Cl.⁷ G11C 7/00

(52) U.S. Cl. 365/226; 365/185.21; 365/189.07; 365/206; 365/207

(58) Field of Search 365/226, 185.21, 365/189.07, 206, 207

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(57) **ABSTRACT**

A memory device including a circuit for actively driving a reference voltage in a memory device is disclosed. A circuit integrated in a memory device and coupled to an external voltage source substantially eliminates fluctuations in the reference voltage of the memory device caused by power supply changes and noise occurring in the memory device by generating a constant voltage and good current drive from the external voltage source.

8 Claims, 3 Drawing Sheets

